L	Hits	Search Text	DB	Time stamp
Number				
1	52	(access near2 pipeline) with storage	USPAT; US-PGPUB; DERWENT; IBM TDB	2004/03/24 18:26
2	1353	(((multiple plurality set) near2 (storage disk RAM DRAM VRAM)) with (bus 'EEE 1394')) and ((multiple plurality set) near2 (processor microprocessor CPU controller))	USPAT; US-PGPUB; DERWENT; IBM_TDB	2004/03/24 18:31
3	433		USPAT; US-PGPUB; DERWENT; IBM_TDB	2004/03/24 18:32
4	338	(((multiple plurality set) near2 (storage disk RAM DRAM VRAM)) with (bus 'EEE 1394')) same ((multiple plurality set) near2 (processor microprocessor CPU controller)) and (concurrent\$2 simultaneous\$2)	USPAT; US-PGPUB; DERWENT; IBM_TDB	2004/03/24 18:37
5	283		USPAT; US-PGPUB; DERWENT; IBM_TDB	2004/03/24 20:23
6	3228	709/219,231.ccls.	USPAT; US-PGPUB; DERWENT; IBM TDB	2004/03/24 20:24
7	7	(storage disk RAM DRAM VRAM)) with (bus 'EEE 1394')) and ((multiple plurality set) near2 (processor microprocessor CPU controller))) and 709/219,231.ccls.	USPAT; US-PGPUB; DERWENT; IBM_TDB	2004/03/24 20:37
8	6	709/214.ccls. and 709/231.ccls.	USPAT; US-PGPUB; DERWENT; IBM TDB	2004/03/24 20:38